













## Nikiforov Andrey Y. Exam Schedule

Wed, 26 Dec	12:45 — 14:20	<b>ATT</b> Unified Information Space Architecture for the Life0circle of an NPP  M18-SH01, M18-SH02, M18-SH03, M18-SH04  ASE
Wed, 26 Dec	14:30 — 16:05	<b>ATT</b> Modern Computer Technology (Open Systems Interconnection)  M17-501  K-807
Thu, 27 Dec	12:45 — 14:20	<b>ATT</b> Modern Computer Technology (Open Systems Interconnection)  M17-504, M17-514  98-6
Thu, 10 Jan	09:00 — 13:00	<b>EXAM</b> Unified Information Space Architecture for the Life0circle of an NPP  M18-SH01, M18-SH02, M18-SH03, M18-SH04  ASE
Wed, 16 Jan	09:00 — 13:00	<b>EXAM</b> Modern Computer Technology (Open Systems Interconnection)  M17-504, M17-514  D-310
Wed, 23 Jan	09:00 — 13:00	<b>EXAM</b> Modern Computer Technology (Open Systems Interconnection)  M17-501  B-213