





















Drevs Yuriy G. Course Schedule



MONDAY

10:15 — 11:50	<input checked="" type="checkbox"/> Academic Practice (research, introduction to the profession)  S18-501  K-213
17:55 — 19:30	<input checked="" type="checkbox"/> LEC System Simulation  M18-502  K-911 <input checked="" type="checkbox"/> SEM System Simulation  M18-502  K-911

THURSDAY

09:30 — 11:50	<input checked="" type="checkbox"/> Academic Practice (research, introduction to the profession)  S18-501  K-212
10:15 — 11:50	<input checked="" type="checkbox"/> LEC Simulations and System Design  S16-501  K-911
12:45 — 14:20	<input checked="" type="checkbox"/> LEC Circuitry of Digital Devices  S16-501  K-911
14:30 — 16:05	<input checked="" type="checkbox"/> SEM Circuitry of Digital Devices  S16-501  K-911 <input checked="" type="checkbox"/> LAB Circuitry of Digital Devices  Subgroup 1  S16-501  K-911
16:15 — 17:50	<input checked="" type="checkbox"/> LAB Circuitry of Digital Devices  Subgroup 2  S16-501  K-911

FRIDAY

12:45 — 14:20	<input checked="" type="checkbox"/> LEC Academic Practice (research, introduction to the profession)  S18-501  K-911
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