









Bakerenkov Aleksandr S. Exam Schedule

Wed, 26 Dec	18:00 — 21:00	ATT Technology and Design of Electronic and Microelectronic Systems  B16-V77  D-311
Fri, 28 Dec	14:30 — 16:05	ATT Design of Integrated Circuits and Systems-on-Chip (SOC)  M17-416  D-303
Thu, 10 Jan	09:00 — 13:00	EXAM Design of Integrated Circuits and Systems-on-Chip (SOC)  M17-416  D-303
Mon, 21 Jan	18:00 — 22:00	EXAM Technology and Design of Electronic and Microelectronic Systems  B16-V77  D-311