












Hlestkov Yuriy A. Course Schedule

MONDAY

10:15 — 11:50	<input checked="" type="checkbox"/> LAB Theoretical Principles of Electrical Engineering  Subgroup 2  S16-162  V-205
	<input checked="" type="checkbox"/> LAB Theoretical Principles of Electrical Engineering  Subgroup 2  S16-161  V-205

TUESDAY

14:30 — 16:05	<input checked="" type="checkbox"/> LAB Theoretical Principles of Electrical Engineering  Subgroup 2  S16-101  V-201
16:15 — 17:50	<input checked="" type="checkbox"/> LAB Theoretical Principles of Electrical Engineering  S16-201  V-205