

Yohin Mihail N. Course Schedule

WEDNESDAY

18:45 — 20:20	<input checked="" type="checkbox"/> LEC	Technology Development of Digital Devices	M17-512, M17-532	B-219
	<input checked="" type="checkbox"/> SEM	Technology Development of Digital Devices	M17-512, M17-532	B-219

THURSDAY

20:25 — 22:00	<input checked="" type="checkbox"/> LEC	Logical Functional Modeling and Automation Design	B15-V71, B15-V72, B15-V73	403
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FRIDAY

18:45 — 20:20	<input checked="" type="checkbox"/> LAB	Logical Functional Modeling and Automation Design	B15-V71	V-215
	<input checked="" type="checkbox"/> LAB	Logical Functional Modeling and Automation Design	B15-V73	V-215
20:25 — 22:00	<input checked="" type="checkbox"/> LAB	Logical Functional Modeling and Automation Design	B15-V72	V-215