

Reshetko Valeriy M. Exam Schedule

Thu, 28 Dec	12:00 — 15:00	ATT	Technology of Chip Projects Design	👥 S13-501	📍 K-911
Thu, 28 Dec	15:00 — 18:00	TEST	Academic (Research) Practice: Introduction to the Profession	👥 S17-501, S17-591	📍 dep.12
Tue, 09 Jan	09:00 — 13:00	EXAM	Technology of Chip Projects Design	👥 S13-501	📍 K-911