

Mescheryakov Vyacheslav V. Exam Schedule

Tue, 26 Dec	12:00 – 15:00	ATT	Computer Methods For Electronic Circuit Design (Part II) 🧑‍🎓 S13-601 📍 K-1017
Wed, 27 Dec	10:15 – 11:50	TEST	Physical Installations: Telecommunication Systems 🧑‍🎓 M16-602 📍 K-1009
Thu, 28 Dec	15:00 – 18:00	TEST	Telecommunication Systems 🧑‍🎓 S13-601 📍 D-305
Fri, 19 Jan	09:00 – 13:00	EXAM	Computer Methods For Electronic Circuit Design (Part II) 🧑‍🎓 S13-601 📍 K-1017