

Mescheryakov Vyacheslav V. Course Schedule

TUESDAY

12:45 — 16:05	<input checked="" type="checkbox"/> SEM Computer Methods For Electronic Circuit Design (Part II) 🧑‍🎓 S13-601 📍 K-1017 <input checked="" type="checkbox"/> LAB Computer Methods For Electronic Circuit Design (Part II) 4 зан. 🧑‍🎓 S13-601 📍 K-1017
16:15 — 17:50	<input checked="" type="checkbox"/> LEC Computer Methods For Electronic Circuit Design (Part II) 🧑‍🎓 S13-601 📍 K-1017

THURSDAY

14:30 — 16:05	<input checked="" type="checkbox"/> LEC Telecommunication Systems 🧑‍🎓 S13-601 📍 D-305
16:15 — 17:50	<input checked="" type="checkbox"/> LEC Physical Installations: Telecommunication Systems (2017-09-07 — 2017-09-28) 🧑‍🎓 M16-602 📍 K-1009 <input checked="" type="checkbox"/> SEM Physical Installations: Telecommunication Systems (2017-10-05 — 2017-12-21) 🧑‍🎓 M16-602 📍 K-1009