









Bakerenkov Aleksandr S. Exam Schedule

Mon, 25 Dec	09:00 — 12:00	ATT Design of Integrated Circuits and Systems-on-Chip (SOC)  M16-406, M17-403  D-408
Thu, 28 Dec	18:00 — 22:00	ATT Technology and Design of Electronic and Microelectronic Systems  B15-V77  D-303
Mon, 22 Jan	09:00 — 13:00	EXAM Design of Integrated Circuits and Systems-on-Chip (SOC)  M16-406  D-408
Wed, 24 Jan	18:00 — 22:00	EXAM Technology and Design of Electronic and Microelectronic Systems  B15-V77  D-303