

## Mescheryakov Vyacheslav V. Exam Schedule

Tue, 23 May	16:15 — 17:50	TEST	Computer Design of Circuit Diagrams 🧑‍🎓 M16-602 📍 K-202
Thu, 25 May	12:00 — 15:00	TEST	Computer Design of Circuit Diagrams (Part I) 🧑‍🎓 S13-601 📍 K-1017