

Mescheryakov Vyacheslav V. Course Schedule

TUESDAY

16:15 — 17:50	☑ SEM	Computer Design of Circuit Diagrams	👥 M16-602	📍 K-205
	☑ LEC	Computer Design of Circuit Diagrams	👥 M16-602	📍 K-202

THURSDAY

10:15 — 11:50	☑ LEC	Computer Design of Circuit Diagrams (Part I)	👥 S13-601	📍 K-1017
11:55 — 13:30	☑ SEM	Computer Design of Circuit Diagrams (Part I)	👥 S13-601	📍 K-1017
14:30 — 17:50	☑ LAB	Computer Design of Circuit Diagrams (Part I) 4 зан.	📍 S13-601	📍 K-1017
	☑ LAB	Computer Design of Circuit Diagrams (Part I) 4 зан.	📍 S13-601	📍 K-1017