

Yohin Mihail N. Course Schedule

MONDAY

10:15 – 11:50	■ LAB	Mainframes and Computer Systems	👥 B14-504	📍 V-215
11:55 – 13:30	■ SEM	Mainframes and Computer Systems	👥 B14-504	📍 B-217

TUESDAY

18:45 – 20:20	☑ LAB	Logical Functional Modeling and Automation Design	📍 Subgroup 2	👥 B14-V71	📍 V-215
	☑ LAB	Logical Functional Modeling and Automation Design	📍 Subgroup 1	👥 B14-V71	📍 V-215
20:25 – 22:00	☑ LAB	Logical Functional Modeling and Automation Design	📍 Subgroup 2	👥 B14-V72	📍 V-215
	☑ LAB	Logical Functional Modeling and Automation Design	📍 Subgroup 1	👥 B14-V72	📍 V-215

THURSDAY

20:25 – 22:00	■ LEC	Logical Functional Modeling and Automation Design	👥 B14-V71, B14-V72	📍 401
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FRIDAY

18:45 – 20:20	☑ LEC	Technology Development of Digital Devices	👥 M16-512, M16-522	📍 V-403
20:25 – 22:00	☑ SEM	Technology Development of Digital Devices	👥 M16-512, M16-522	📍 V-409