

Shaltaeva Yuliya R. Exam Schedule

Thu, 13 Apr	10:15 — 11:50	TEST	Design of Integrated Circuits and Systems-on-Chip (SOC)	👥 M15-405	📍 D-303
Thu, 04 May	10:15 — 11:50	ATT	Design of Integrated Circuits and Systems-on-Chip (SOC)	👥 M16-403	📍 D-303
Thu, 18 May	09:00 — 13:00	EXAM	Design of Integrated Circuits and Systems-on-Chip (SOC)	👥 M16-403	📍 D-303