







## Shaltaeva Yuliya R. Course Schedule

### THURSDAY

08:30 — 10:05	<span style="border: 1px solid black; padding: 2px;">■ LAB</span> Design of Integrated Circuits and Systems-on-Chip (SOC) <b>(2017-03-02 — 2017-04-27)</b>  M16-403  D-303
10:15 — 11:50	<span style="border: 1px solid black; padding: 2px;">■ LEC</span> Design of Integrated Circuits and Systems-on-Chip (SOC) <b>(2017-02-09 — 2017-02-23)</b>  M15-405, M16-403  D-303 <span style="border: 1px solid black; padding: 2px;">■ SEM</span> Design of Integrated Circuits and Systems-on-Chip (SOC) <b>(2017-03-02 — 2017-04-27)</b>  M15-405, M16-403  D-303