

Yohin Mihail N. Course Schedule

TUESDAY

12:45 — 14:20	■ LAB	Microprocessor Devices and Systems 🧑‍🎓 B21-513, B21-563 📍 V-215
17:05 — 18:40	■ LAB	Electrician, Electronic Engineering and Circuitry (Circuitry). 🗣️ Subgroup 2 🧑‍🎓 B22-513 📍 V-215
18:45 — 20:20	■ LAB	Electrician, Electronic Engineering and Circuitry (Circuitry). 🗣️ Subgroup 1 🧑‍🎓 B22-513 📍 V-215

WEDNESDAY

14:30 — 16:05	■ LAB	Microprocessor Devices and Systems 🗣️ Subgroup 1 🧑‍🎓 B21-503 📍 V-215
16:15 — 17:50	■ LEC	Logical Functional Modeling and Automation Design 🧑‍🎓 B21-503, B21-513, B21-563 📍 402
18:45 — 20:20	■ LAB	Microprocessor Devices and Systems 🗣️ Subgroup 2 🧑‍🎓 B21-503 📍 V-215
20:25 — 22:00	■ LEC	Technology Development of Digital Devices 🧑‍🎓 M23-512, M23-585, M23-V01 📍 V-403

THURSDAY

14:30 — 16:05	☑️ LAB	Logical Functional Modeling and Automation Design 🧑‍🎓 B21-513, B21-563 📍 V-215
	☑️ LAB	Microprocessor Devices and Systems 🧑‍🎓 B21-513, B21-563 📍 V-215
16:15 — 17:50	■ LEC	Microprocessor Devices and Systems 🧑‍🎓 B21-503, B21-513, B21-563 📍 B-218
18:45 — 20:20	☑️ LAB	Microprocessor Devices and Systems 🗣️ Subgroup 2 🧑‍🎓 B21-503 📍 V-215
	☑️ LAB	Microprocessor Devices and Systems 🗣️ Subgroup 1 🧑‍🎓 B21-503 📍 V-215
20:25 — 22:00	☑️ LAB	Logical Functional Modeling and Automation Design 🗣️ Subgroup 2 🧑‍🎓 B21-503 📍 V-215
	☑️ LAB	Logical Functional Modeling and Automation Design 🗣️ Subgroup 1 🧑‍🎓 B21-503 📍 V-215