

Rahmatulin Aleksandr B. Exam Schedule

Mon, 23 Dec	09:20 — 11:00	ATT Computer-aided design and construction of electronic components and systems B22-612 I-201
Mon, 23 Dec	13:35 — 15:15	TEST Practicum Project B24-605 I-204
Mon, 23 Dec	15:20 — 17:00	ATT Workshop: basics of designing cyber-physical devices and systems B22-603 VNIIA
Tue, 24 Dec	12:45 — 14:20	ATT Computer-aided design and construction of electronic components and systems B22-603 I-201
Tue, 24 Dec	16:15 — 17:50	TEST B24-604 I-204
Wed, 25 Dec	11:55 — 13:30	ATT Computer-aided design and construction of electronic components and systems B22-605 I-201
Wed, 25 Dec	14:30 — 16:05	TEST Practicum Project B24-603 I-201
Thu, 26 Dec	10:15 — 11:50	TEST Practicum Project B23-605 I-204
Thu, 26 Dec	11:55 — 13:30	TEST Automated Design of Electronic Components and Systems S22-103 I-201
Fri, 27 Dec	10:15 — 11:50	TEST Practicum Project S24-602 I-204
Fri, 27 Dec	11:55 — 14:20	TEST Practicum Project B23-612 I-204
Fri, 27 Dec	15:20 — 17:50	TEST B23-604 I-204
Sat, 28 Dec	14:30 — 16:05	ATT Computer-aided design and construction of electronic components and systems S22-602 I-201