





## Belyakov Vladimir V. Exam Schedule

Mon, 11 Mar	11:55 — 14:20	<b>ATT</b> Design of Integrated Circuits and Systems-on-Chip (SOC)  M22-406, M22-423  D-407
Fri, 22 Mar	09:00 — 13:00	<b>EXAM</b> Design of Integrated Circuits and Systems-on-Chip (SOC)  M22-406, M22-423  D-407