

Yohin Mihail N. Exam Schedule

Mon, 22 Apr	14:30 — 17:00		Design of Digital Devices Based on Programmable Logic Devices 🧑‍🎓 M22-522 📍 MTSST
Thu, 23 May	12:45 — 15:15	ATT	Mainframes and Computer Systems 🧑‍🎓 B21-513, B21-563 📍 V-215
Thu, 23 May	12:45 — 15:15	ATT	Mainframes and Computer Systems 🧑‍🎓 B21-513, B21-563 📍 DOT
Thu, 23 May	18:45 — 20:20	ATT	Logical Functional Modeling and Automation Design 🧑‍🎓 B20-V02, B21-V71 📍 V-215
Sat, 08 Jun	09:00 — 13:00	EXAM	Mainframes and Computer Systems 🧑‍🎓 B21-503, B21-513, B21-563 📍 DOT
Fri, 21 Jun	18:00 — 22:00	EXAM	Logical Functional Modeling and Automation Design 🧑‍🎓 B20-V02, B21-V71 📍 DOT