

Yohin Mihail N. Course Schedule

MONDAY

14:30 — 17:00	■ LEC Design of Digital Devices Based on Programmable Logic Devices (2024-02-12 — 2024-03-11) 🧑‍🎓 M22-522 📍 MTSST ■ SEM Design of Digital Devices Based on Programmable Logic Devices (2024-03-18 — 2024-04-15) 🧑‍🎓 M22-522 📍 MTSST
---------------	--

THURSDAY

16:15 — 18:40	■ LAB Mainframes and Computer Systems 🧑‍🎓 B21-513, B21-563 📍 V-215
18:45 — 20:20	■ LEC Logical Functional Modeling and Automation Design 🧑‍🎓 B20-V02, B21-V71 📍 B-210
20:25 — 22:00	▣ LAB Logical Functional Modeling and Automation Design ⓘ Subgroup 2 🧑‍🎓 B20-V02, B21-V71 📍 V-215

FRIDAY

20:25 — 22:00	▣ LAB Logical Functional Modeling and Automation Design ⓘ Subgroup 1 🧑‍🎓 B20-V02 📍 V-215
---------------	---