













Stenin Vladimir Y. Exam Schedule

Thu, 22 Dec	15:00 — 18:00	ATT	Elements of Analog-to-Digital Systems (Hybrid Systems)  S13-401, S13-402  D-314
Tue, 27 Dec	09:00 — 12:00	TEST	Scientific Research Work  S13-402  dep.3
Tue, 27 Dec	12:00 — 15:00	TEST	Scientific Research Work  S13-401  dep.3
Wed, 28 Dec	12:00 — 15:00	TEST	Scientific Research Work  S12-402  dep.3
Wed, 28 Dec	15:00 — 18:00	TEST	Scientific Research Work  S12-401  dep.3
Mon, 16 Jan	09:00 — 13:00	EXAM	Elements of Analog-to-Digital Systems (Hybrid Systems)  S13-401, S13-402  V-411