

Voronov Yuriy A. Exam Schedule

Tue, 22 Dec	18:45 — 20:20	ATT	Theoretical Principles of Integrated Circuits Technology 🗺 B18-V73 📍 DOT
Tue, 22 Dec	20:25 — 22:00	TEST	Principles of Optoelectronics 🗺 B18-V73 📍 DOT
Mon, 28 Dec	14:30 — 16:05	TEST	Principles of Optoelectronics 🗺 S16-401 📍 DOT
Fri, 22 Jan	18:00 — 22:00	EXAM	Theoretical Principles of Integrated Circuits Technology 🗺 B18-V73 📍 DOT