

Reshetko Valeriy M. Exam Schedule

Tue, 22 Dec	16:15 — 17:50	ATT	Circuitry of Digital Devices 🧑‍🎓 S18-501 📍 ДОТ
Thu, 24 Dec	11:55 — 13:30	ATT	Technology of Chip Projects Design 🧑‍🎓 S16-501 📍 ДОТ
Tue, 19 Jan	09:00 — 13:00	EXAM	Technology of Chip Projects Design 🧑‍🎓 S16-501 📍 ДОТ