

Reshetko Valeriy M. Course Schedule

TUESDAY

14:30 — 16:05	▣ LAB	Circuitry of Digital Devices	👥 S18-501	📍 ДОТ
---------------	-------	------------------------------	-----------	-------

THURSDAY

11:55 — 13:30	■ LEC	Technology of Chip Projects Design	👥 S16-501	📍 ДОТ
13:35 — 15:15	■ LAB	Technology of Chip Projects Design	👥 S16-501	📍 ДОТ
16:15 — 17:50	■ SEM	Project Workshop (Introduction To Profession) (2020-09-03 — 2020-11-19)	👥 S20-501	📍 ДОТ