

# Yohin Mihail N. Course Schedule

## MONDAY

10:15 — 11:50	☑ LEC	Design of microprocessor systems (2020-02-17 — 2020-05-25) 🧑‍🎓 M19-SH05 📍 V-215
11:55 — 13:30	☑ SEM	Design of microprocessor systems (2020-02-17 — 2020-05-25) 🧑‍🎓 M19-SH05 📍 V-215

## TUESDAY

20:25 — 22:00	■ LEC	Logical Functional Modeling and Automation Design (2020-02-11 — 2020-03-17) 🧑‍🎓 B17-V71, B17-V72 📍 B-208
	■ LEC	Logical Functional Modeling and Automation Design (2020-03-24 — 2020-05-26) 🧑‍🎓 B17-V71, B17-V72 📍 dep.12

## FRIDAY

10:15 — 13:30	■ LAB	Research Project: Computer Hardware (2020-02-07 — 2020-03-27) 🧑‍🎓 B16-503 📍 dep.12
14:30 — 17:50	■ LAB	Research Project: Computer Hardware (2020-02-07 — 2020-03-27) 🧑‍🎓 B16-503 📍 dep.12
18:45 — 22:00	☑ LAB	Logical Functional Modeling and Automation Design 4 зан. 📍 B17-V72 📍 V-215 ⚠ Subgroup 1 (2020-02-07 — 2020-05-29)
	☑ LAB	Logical Functional Modeling and Automation Design 4 зан. 📍 B17-V72 📍 V-215 ⚠ Subgroup 2 (2020-02-07 — 2020-05-29)
	☑ LAB	Logical Functional Modeling and Automation Design 4 зан. 📍 B17-V71 📍 V-215 ⚠ Subgroup 1 (2020-02-14 — 2020-03-13)
	☑ LAB	Logical Functional Modeling and Automation Design 4 зан. 📍 B17-V71 📍 V-215 ⚠ Subgroup 2 (2020-02-14 — 2020-03-13)
	☑ LAB	Logical Functional Modeling and Automation Design 4 зан. 📍 B17-V71 📍 dep.12 ⚠ Subgroup 1 (2020-03-27 — 2020-05-22)
	☑ LAB	Logical Functional Modeling and Automation Design 4 зан. 📍 B17-V71 📍 dep.12 ⚠ Subgroup 2 (2020-03-27 — 2020-05-22)