

Mescheryakov Vyacheslav V. Course Schedule

MONDAY

10:15 — 11:50	■	LEC	Telecommunication Systems 🐾 S14-601 📍 K-1006
12:45 — 14:20	☑	LEC	Computer Design of Circuit Diagrams 🐾 S14-601 📍 K-1009
12:45 — 16:05	☑	SEM	Computer Design of Circuit Diagrams 🐾 S14-601 📍 K-1017
14:30 — 16:05	☑	LAB	Computer Design of Circuit Diagrams 🐾 S14-601 📍 K-1017

FRIDAY

08:30 — 11:50	■	SEM	Scientific Research Work 🐾 S15-601 📍 dep.26
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SATURDAY

11:55 — 14:20	■	SEM	Scientific Research Work 🐾 S14-601 📍 dep.26
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