

Reshetko Valeriy M. Exam Schedule

Mon, 24 Dec	15:00 — 18:00	ATT	Technology of Chip Projects Design	👥 S14-501	📍 K-911
Tue, 25 Dec	15:00 — 18:00	ATT	Industry Workshop: research, methods and means of automated systems for information processing and management	👥 S14-501	📍 dep.12
Thu, 27 Dec	09:00 — 13:00	EXAM	Technology of Chip Projects Design	👥 S14-501	📍 K-315