

Reshetko Valeriy M. Course Schedule

MONDAY

10:15 — 11:50	■ Academic Practice (research, introduction to the profession) 🧑‍🎓 S18-501 📍 K-213
16:15 — 17:50	■ LAB Technology of Chip Projects Design 🧑‍🎓 S14-501 📍 K-911

TUESDAY

14:30 — 16:05	■ Industry Workshop: research, methods and means of automated systems for information processing and management 🧑‍🎓 S14-501 📍 dep.12
16:15 — 17:50	■ SEM Industry Workshop: research, methods and means of automated systems for information processing and management 🧑‍🎓 S14-501 📍 dep.12

WEDNESDAY

10:15 — 11:50	■ LEC Technology of Chip Projects Design 🧑‍🎓 S14-501 📍 K-911
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THURSDAY

09:30 — 11:50	☑ Academic Practice (research, introduction to the profession) 🧑‍🎓 S18-501 📍 K-212
14:30 — 16:05	☑ LAB Circuitry of Digital Devices ⓘ Subgroup 1 🧑‍🎓 S16-501 📍 K-911
16:15 — 17:50	☑ LAB Circuitry of Digital Devices ⓘ Subgroup 2 🧑‍🎓 S16-501 📍 K-911

FRIDAY

12:45 — 14:20	■ LEC Academic Practice (research, introduction to the profession) 🧑‍🎓 S18-501 📍 K-911
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