















## Exams for group S16-401

Tue, 17 Dec	16:15 — 17:50	TEST	Principles of Professional Communication in a Foreign Language  Polyakova E.I. 📍 B-403
Mon, 23 Dec	09:00 — 12:00	TEST	Economics of digital design and engineering in nuclear industry  Smirnov D.S., Putilov A.V. 📍 A-100
Mon, 23 Dec	12:00 — 15:00	ATT	Digital and analog integrated circuits  Chernikov A.V. 📍 D-311
Tue, 24 Dec	09:00 — 12:00	TEST	Automated Design Systems in Microelectronics  Butuzov V.A. 📍 T-102
Tue, 24 Dec	12:00 — 15:00	TEST	Materials Science  Mihalchik V.V., Tenishev A.V. 📍 T-208
Wed, 25 Dec	09:00 — 12:00	ATT	Integrated circuit technology and nanotechnology  Veselov D.S. 📍 D-418
Wed, 25 Dec	12:00 — 15:00	ATT	Physics of Microelectronic Structures  Pershenkov V.S. 📍 D-303
Thu, 26 Dec	09:00 — 12:00	ATT	Microprocessor Systems  Shaltaeva Y.R. 📍 V-404
Thu, 26 Dec	12:00 — 15:00	TEST	Design of Integrated Circuits  Popov V.D. 📍 D-303
Fri, 27 Dec	09:00 — 12:00	TEST	Practical Training (Research Work)  Shaltaeva Y.R. 📍 D-311
Mon, 13 Jan	09:00 — 13:00	EXAM	Integrated circuit technology and nanotechnology  Veselov D.S. 📍 D-418
Fri, 17 Jan	09:00 — 13:00	EXAM	Digital and analog integrated circuits  Chernikov A.V. 📍 D-311
Tue, 21 Jan	09:00 — 13:00	EXAM	Physics of Microelectronic Structures  Pershenkov V.S. 📍 B-208
Sat, 25 Jan	09:00 — 13:00	EXAM	Microprocessor Systems  Tihonov Y.N. 📍 V-404