

















## Course Schedule, group S14-404















### MONDAY

08:30 — 10:05	<input checked="" type="checkbox"/> SEM Scientific Research Work  Belyakov V.V., Shaltaeva Y.R., Bakerenkov A.S., Shagurin I.I., Popov V.D., Pershenkov V.S., Lebedev A.A., Veselov D.S.  dep.27 <input checked="" type="checkbox"/> SEM Microcircuits. Digital Integrated Circuits  Shagurin I.I.  D-311
10:15 — 11:50	<input checked="" type="checkbox"/> LEC Microprocessor Systems  Tihonov Y.N.  403 <input checked="" type="checkbox"/> LEC Microcircuits. Digital Integrated Circuits  Shagurin I.I.  V-417
12:45 — 14:20	<input checked="" type="checkbox"/> SEM Scientific Research Work  Lebedev A.A., Pershenkov V.S., Shagurin I.I., Popov V.D., Shaltaeva Y.R., Bakerenkov A.S., Belyakov V.V., Veselov D.S.  dep.27
14:30 — 16:05	<input checked="" type="checkbox"/> LEC Economics of Nuclear Industry Design and Construction <b>(2017-09-04 — 2017-11-20)</b>  Pimenova V.O., Bugaenko M.V., Berestov A.V., Baryshev G.K., Putilov A.V., Timohin D.V., Smirnov D.S., Chervyakov V.N.  A-100
16:15 — 17:50	<input checked="" type="checkbox"/> LAB Economics of Nuclear Industry Design and Construction <b>(2017-09-04 — 2017-11-20)</b>  Pimenova V.O., Bugaenko M.V., Berestov A.V., Baryshev G.K., Putilov A.V., Timohin D.V., Chervyakov V.N., Smirnov D.S.  A-100









### TUESDAY

08:30 — 10:05	<input checked="" type="checkbox"/> LEC Opt Military Training  dep.20
10:15 — 17:00	<input checked="" type="checkbox"/> SEM Opt Military Training  dep.20












### WEDNESDAY

08:30 — 10:05	<input checked="" type="checkbox"/> LEC Designing Microelectronic Systems for Physical Installations  Patrikeev L.N.  K-307
10:15 — 11:50	<input checked="" type="checkbox"/> LEC Design of Integrated Circuits  Popov V.D.  D-311
12:45 — 14:20	<input checked="" type="checkbox"/> SEM Designing Microelectronic Systems for Physical Installations  Patrikeev L.N.  D-311 <input checked="" type="checkbox"/> LAB Design of Integrated Circuits  Popov V.D.  D-303
14:30 — 16:05	<input checked="" type="checkbox"/> SEM Software and Hardware Support of PCs  Gorbunov M.S.  D-303 <input checked="" type="checkbox"/> SEM Design of Integrated Circuits  Popov V.D.  D-303
16:15 — 17:50	<input checked="" type="checkbox"/> LEC Software and Hardware Support of PCs  Gorbunov M.S.  V-417







### THURSDAY

08:30 — 10:05	<input checked="" type="checkbox"/> SEM Principles of Professional Communication in a Foreign Language  Selezneva T.V.  B-401
10:15 — 11:50	<input checked="" type="checkbox"/> LEC Technology of Integrated Circuits  Veselov D.S.  V-411
12:45 — 14:20	<input checked="" type="checkbox"/> SEM Technology of Integrated Circuits  Veselov D.S.  D-311
14:30 — 17:50	<input checked="" type="checkbox"/> LAB Technology of Integrated Circuits 4 зан.  Voronov Y.A., Veselov D.S.  D-418

### FRIDAY

08:30 — 10:05	<input checked="" type="checkbox"/> SEM Physical Education (Elective Discipline)  dep.15/2
10:15 — 11:50	<input checked="" type="checkbox"/> LEC Microprocessor Systems  Tihonov Y.N.  406
12:45 — 14:20	<input checked="" type="checkbox"/> LAB Microcircuits. Digital Integrated Circuits  Tihonov Y.N.  D-303 <input checked="" type="checkbox"/> LAB Microprocessor Systems  Tihonov Y.N.  D-303
14:30 — 16:05	<input checked="" type="checkbox"/> LEC Microcircuits. Digital Integrated Circuits  Shagurin I.I.  V-411
16:15 — 17:50	<input checked="" type="checkbox"/> SEM Scientific Research Work  Shaltaeva Y.R., Lebedev A.A., Veselov D.S., Belyakov V.V., Pershenkov V.S., Bakerenkov A.S., Popov V.D., Shagurin I.I.  dep.27

### SATURDAY

11:55 — 13:30	<input checked="" type="checkbox"/> LEC Automated Design Systems in Microelectronics  Butuzov V.A.  D-311
13:35 — 15:15	<input checked="" type="checkbox"/> LAB Automated Design Systems in Microelectronics  Butuzov V.A.  D-303 <input checked="" type="checkbox"/> SEM Automated Design Systems in Microelectronics  Butuzov V.A.  D-303