

Course Schedule, group M23-512

MONDAY

08:30 — 10:05	■	LEC	Opt	Military Training	📍 dep.20
10:15 — 17:00	■	SEM	Opt	Military Training	📍 dep.20

TUESDAY

16:15 — 17:50	■	SEM		Foreign Language (Special Course)	👤 Subgroup 1 🎓 Bryzgalova S.A. 📍 A-207
	■	SEM		Foreign Language (Special Course)	👤 Subgroup 2 🎓 Zhavoronkova D.G. 📍 B-213
18:00 — 20:00	■	Reserved (2024-06-25) 🎓 Yadykin I.M. 📍 V-411 Dep. 12			
18:45 — 20:20	☑	LEC	Elect	Electrician, Electronic Engineering and Circuitry (Circuitry).	🎓 Yadykin I.M. 📍 V-403
	☑	SEM	Elect	Electrician, Electronic Engineering and Circuitry (Circuitry).	🎓 Yadykin I.M. 📍 V-403

WEDNESDAY

15:20 — 17:00	☑	LAB		Intelligent Systems	👤 Subgroup 3 🎓 Kudryavtsev K.Y., Chepin E.V., Gridnev A.A. 📍 V-210b
17:05 — 18:40	■	LAB	Elect	Electrician, Electronic Engineering and Circuitry (Circuitry).	🎓 Yadykin I.M. 📍 V-215
18:45 — 20:20	■	LEC		Information and Coding Theory (Special Chapter)	🎓 Berezkin E.F. 📍 K-307
20:25 — 22:00	■	LEC		Concurrent Computing	🎓 Shuvalov V.B. 📍 K-307

THURSDAY

18:45 — 20:20	☑	LAB		Concurrent Computing	👤 Subgroup 1 🎓 Shuvalov V.B. 📍 K-911
	☑	LAB		Intelligent Systems	👤 Subgroup 2 🎓 Dyumin A.A., Kudryavtsev K.Y. 📍 V-204b
	☑	LAB		Information and Coding Theory (Special Chapter)	👤 Subgroup 1 🎓 Berezkin E.F., Saprykina V.S. 📍 K-911
20:25 — 22:00	☑	LAB		Intelligent Systems	👤 Subgroup 1 🎓 Kudryavtsev K.Y., Dyumin A.A. 📍 V-204b
	☑	LAB		Concurrent Computing	👤 Subgroup 2 🎓 Shuvalov V.B. 📍 K-911
	☑	LAB		Information and Coding Theory (Special Chapter)	👤 Subgroup 2 🎓 Berezkin E.F., Saprykina V.S. 📍 K-911

FRIDAY

15:20 — 17:00	■	PR		Industry Workshop: Pedagogy	🎓 Vavrenyuk A.B. 📍 dep.12
17:05 — 18:40	■	SEM		Optimization Methods	🎓 Ponomareva Y.P. 📍 B-212
18:45 — 20:20	■	LEC		Intelligent Systems	🎓 Dyumin A.A., Kudryavtsev K.Y., Chepin E.V. 📍 V-403
20:25 — 22:00	■	LEC		Technology of Software Development	🎓 Shurygin V.A. 📍 V-403

SATURDAY

11:05 — 12:40	■	LAB	Elect	Digital Circuit Synthesis	👤 Subgroup 1 🎓 Skitev A.A. 📍 V-215
12:45 — 14:20	☑	LEC	Elect	Digital Circuit Synthesis	🎓 Skitev A.A. 📍 V-215
	☑	SEM	Elect	Digital Circuit Synthesis	🎓 Skitev A.A. 📍 V-215
14:30 — 16:05	■	LAB	Elect	Digital Circuit Synthesis	👤 Subgroup 2 🎓 Skitev A.A. 📍 V-215