



















Exams for group M19-483

Tue, 15 Dec	16:15 — 17:50	TEST	Scientific Programs of Modern Times  Shmonin D.V.  ДОТ
Fri, 18 Dec	16:15 — 17:50	TEST	Methodology of Science  Mironova N.B.  ДОТ
Tue, 22 Dec	10:15 — 11:50	TEST	Elect Optoelectronics  Bakerenkov A.S.  ДОТ
Tue, 22 Dec	14:30 — 16:05	TEST	Elect Microsystems  Demidova A.V., Boychenko D.V.  ДОТ
Thu, 24 Dec	12:45 — 14:20	TEST	Educational practice: design and technology  ДОТ
Thu, 24 Dec	14:30 — 16:05	ATT	Electronic Systems Design  Boychenko D.V.  ДОТ
Fri, 25 Dec	12:45 — 14:20	TEST	Practical Training (Research Work)  ДОТ
Fri, 25 Dec	14:30 — 16:05	ATT	Design of Integrated Circuits and Systems-on-Chip (SOC)  Bakerenkov A.S.  ДОТ
Wed, 13 Jan	09:00 — 13:00	EXAM	Electronic Systems Design  Boychenko D.V.  ДОТ
Mon, 18 Jan	09:00 — 13:00	EXAM	Design of Integrated Circuits and Systems-on-Chip (SOC)  Bakerenkov A.S.  ДОТ