

**Course Schedule, room V-215****MONDAY**

10:15 — 11:50	▣ LAB	Information Security	📍 Subgroup 1	👥 B21-503	🎓 Shurygin V.A.
	▣ LAB	Information Security	📍 Subgroup 2	👥 B21-503	🎓 Shurygin V.A.
12:45 — 14:20	▣ LAB	Information Security: Cryptographic Tools	📍 Subgroup 1	👥 S21-712	🎓 Shurygin V.A.
	▣ LAB	Information Security: Cryptographic Tools		👥 S21-702	🎓 Shurygin V.A.
14:30 — 16:05	▣ LAB	Information Security: Cryptographic Tools	📍 Subgroup 2	👥 S21-712, S21-762	🎓 Shurygin V.A.
16:15 — 18:40	▣ LAB	Mainframes and Computer Systems	📍 Subgroup 2	👥 B21-503	🎓 Novikov G.G., Yadykin I.M.

**TUESDAY**

10:15 — 11:50	▣ LAB	Information Security: Cryptographic Tools		👥 S20-703	🎓 Shurygin V.A.
12:45 — 14:20	▣ LAB	Automata Theory	📍 Subgroup 2	👥 B22-513	🎓 Yadykin I.M.
	▣ LAB	Automata Theory	📍 Subgroup 1	👥 B22-513	🎓 Yadykin I.M.
14:30 — 16:05	▣ LAB	Programming (Algorithms and Data Structures)	📍 Subgroup 1	👥 B23-516	🎓 Shevchenko N.A.
20:25 — 22:00	▣ LAB	Electrician, Electronic Engineering and Circuitry (Circuitry).	📍 Subgroup 2	👥 M23-502, M23-V01	🎓 Yadykin I.M.

**WEDNESDAY**

09:20 — 11:50	▣ LAB	Mainframes and Computer Systems	📍 Subgroup 1	👥 B21-503	🎓 Novikov G.G., Yadykin I.M.
14:30 — 16:05	▣ LAB	Information Security		👥 B21-513, B21-563, S21-501	🎓 Shurygin V.A.
17:05 — 18:40	▣ LAB	Electrician, Electronic Engineering and Circuitry (Circuitry).		👥 M23-512	🎓 Yadykin I.M.
20:25 — 22:00	▣ LAB	Electrician, Electronic Engineering and Circuitry (Circuitry).	📍 Subgroup 2	👥 B21-V02	🎓 Yadykin I.M., Novikov G.G.
	▣ LAB	Electrician, Electronic Engineering and Circuitry (Circuitry).	📍 Subgroup 1	👥 B21-V02	🎓 Yadykin I.M., Novikov G.G.

**THURSDAY**

12:45 — 14:20	▣ LAB	Automata Theory	📍 Subgroup 1	👥 B22-503	🎓 Yadykin I.M.
	▣ LAB	Automata Theory	📍 Subgroup 2	👥 B22-503	🎓 Yadykin I.M.
16:15 — 18:40	▣ LAB	Mainframes and Computer Systems		👥 B21-513, B21-563	🎓 Yohin M.N.
20:25 — 22:00	▣ LAB	Logical Functional Modeling and Automation Design	📍 Subgroup 2	👥 B20-V02, B21-V71	🎓 Yohin M.N.
	▣ LAB	Electrician, Electronic Engineering and Circuitry (Circuitry).		👥 B22-V71	🎓 Novikov G.G., Yadykin I.M.

**FRIDAY**

17:05 — 18:40	▣ LAB	Electrician, Electronic Engineering and Circuitry (Circuitry).	📍 Subgroup 1	👥 M23-502	🎓 Yadykin I.M.
20:25 — 22:00	▣ LAB	Logical Functional Modeling and Automation Design	📍 Subgroup 1	👥 B20-V02	🎓 Yohin M.N.

**SATURDAY**

11:05 — 12:40	▣ LAB	Digital Circuit Synthesis	📍 Subgroup 1	👥 M23-512	🎓 Skitev A.A.
12:45 — 14:20	▣ LEC	Digital Circuit Synthesis		👥 M23-512	🎓 Skitev A.A.
	▣ SEM	Digital Circuit Synthesis		👥 M23-512	🎓 Skitev A.A.
14:30 — 16:05	▣ LAB	Digital Circuit Synthesis	📍 Subgroup 2	👥 M23-512	🎓 Skitev A.A.