

## Course Schedule, room K-911

### MONDAY

16:15 — 17:50	■ LAB	Technology of Chip Projects Design 🐾 S14-501 🎓 Reshetko V.M.
17:55 — 19:30	■ LEC	System Simulation 🐾 M18-502 🎓 Drevs Y.G.
	■ SEM	System Simulation 🐾 M18-502 🎓 Drevs Y.G.

### WEDNESDAY

10:15 — 11:50	■ LEC	Technology of Chip Projects Design 🐾 S14-501 🎓 Reshetko V.M., Zaeva M.A.
12:45 — 13:30	■ LEC	Principles of Digital Data Processing Application and Theory 🐾 S14-501 🎓 Zaeva M.A.
13:35 — 15:15	■ LAB	Principles of Digital Data Processing Application and Theory 🐾 S14-501 🎓 Zaeva M.A.
15:20 — 17:00	■ LEC	Special Training <b>(2018-12-19)</b> 🐾 S14-501 🎓 Kulik S.D., Aronov A.G.
17:05 — 18:40	■ SEM	Information Security: Cryptographic Tools 🐾 M18-522 🎓 Chugunkov I.V.
18:45 — 22:00	■ SEM	Scientific Research Organization (Hardware Support) <b>(2018-09-05 — 2018-10-17)</b> 🐾 B15-V73 🎓 Kudryavtsev K.Y.

### THURSDAY

10:15 — 11:50	■ LEC	Simulations and System Design 🐾 S16-501 🎓 Drevs Y.G., Shevchenko N.A.
12:45 — 14:20	■ LEC	Circuitry of Digital Devices 🐾 S16-501 🎓 Drevs Y.G.
14:30 — 16:05	■ SEM	Circuitry of Digital Devices 🐾 S16-501 🎓 Drevs Y.G.
	■ LAB	Circuitry of Digital Devices 🗣 Subgroup 1 🐾 S16-501 🎓 Drevs Y.G., Reshetko V.M.
16:15 — 17:50	■ LAB	Circuitry of Digital Devices 🗣 Subgroup 2 🐾 S16-501 🎓 Drevs Y.G., Reshetko V.M.
17:05 — 18:40	■ SEM	Digital Signal Processing 🐾 M17-502 🎓 Zaeva M.A.
18:45 — 20:20	■ LAB	Digital Signal Processing 🗣 Subgroup 1 🐾 M17-502 🎓 Zaeva M.A.
	■ LAB	Digital Signal Processing 🗣 Subgroup 2 🐾 M17-502 🎓 Zaeva M.A.

### FRIDAY

08:30 — 10:05	■ LAB	Technology and Systems Integration Standards <b>(2018-12-21)</b> 🐾 S14-501 🎓 Alekseenko I.V.
10:15 — 11:50	■ LEC	Technology and Systems Integration Standards <b>(2018-12-21)</b> 🐾 S14-501 🎓 Alekseenko I.V., Shuvalov V.B.
12:45 — 14:20	■ LEC	Academic Practice (research, introduction to the profession) 🐾 S18-501 🎓 Drevs Y.G., Berezkin E.F., Shuvalov V.B., Reshetko V.M., Ovcharenko E.S., Kulik S.D., Krasnikova S.A., Zaeva M.A., Sergeev M.S., Evstifeev A.A.