

Course Schedule, room V-215

TUESDAY

08:30 — 10:05	<input checked="" type="checkbox"/> LAB	Information Security	❗ Subgroup 1		B16-503		Ivanov M.A.
17:05 — 18:40	<input checked="" type="checkbox"/> LAB	Electrician, Electronic Engineering and Circuitry (Circuitry).	❗ Subgroup 2		M18-512		Skitev A.A.
18:45 — 20:20	<input checked="" type="checkbox"/> SEM	Electrician, Electronic Engineering and Circuitry (Circuitry).			M18-512		Yadykin I.M., Skitev A.A.

WEDNESDAY

11:55 — 13:30	<input checked="" type="checkbox"/> SEM	Cryptographic Information Security		S15-503		Chugunkov I.V.	
12:45 — 14:20	<input checked="" type="checkbox"/> LAB	Modern Payment Systems and Their Security		S14-504		Chugunkov I.V.	
14:30 — 16:05	<input checked="" type="checkbox"/> LAB	Mainframes and Computer Systems	❗ Subgroup 2		B16-503		Yadykin I.M.
16:15 — 17:50	<input checked="" type="checkbox"/> LAB	Mainframes and Computer Systems	❗ Subgroup 1		B16-503		Novikov G.G.

THURSDAY

08:30 — 10:05	<input checked="" type="checkbox"/> LAB	Information Security		S16-501		Shurygin V.A.	
10:15 — 11:50	<input checked="" type="checkbox"/> LAB	Automata Theory	❗ Subgroup 2		B17-503		Lyubomudrov A.A., Yadykin I.M.
	<input checked="" type="checkbox"/> LAB	Automata Theory	❗ Subgroup 1		B17-503		Yadykin I.M., Lyubomudrov A.A.
12:45 — 14:20	<input checked="" type="checkbox"/> LAB	Cryptographic Information Security		S15-503		Shurygin V.A.	
14:30 — 16:05	<input checked="" type="checkbox"/> LAB	Modern Payment Systems and Their Security		S15-503		Chugunkov I.V.	
18:45 — 20:20	<input checked="" type="checkbox"/> LAB	Electrician, Electronic Engineering and Circuitry (Circuitry).	❗ Subgroup 2		B17-V71		Yadykin I.M.
	<input checked="" type="checkbox"/> LAB	Electrician, Electronic Engineering and Circuitry (Circuitry).	❗ Subgroup 1		B17-V71		Novikov G.G.
20:25 — 22:00	<input checked="" type="checkbox"/> LAB	Electrician, Electronic Engineering and Circuitry (Circuitry).	❗ Subgroup 2		B17-V72		Yadykin I.M.
	<input checked="" type="checkbox"/> LAB	Electrician, Electronic Engineering and Circuitry (Circuitry).	❗ Subgroup 1		B17-V72		Novikov G.G.

FRIDAY

10:15 — 11:50	<input checked="" type="checkbox"/> LAB	Information Security		B16-513		Shurygin V.A.	
12:45 — 14:20	<input checked="" type="checkbox"/> LAB	Information Security: Cryptographic Tools	❗ Subgroup 1		S16-502		Shurygin V.A.
	<input checked="" type="checkbox"/> LAB	Information Security: Cryptographic Tools			S16-562		Shurygin V.A.
18:45 — 22:00	<input checked="" type="checkbox"/> LAB	Logical Functional Modeling and Automation Design 4 зан.			B16-V75		Yohin M.N.

SATURDAY

10:15 — 11:50	<input checked="" type="checkbox"/> LAB	Mainframes and Computer Systems		B16-513		Skitev A.A.
11:55 — 15:15	<input checked="" type="checkbox"/> LAB	Logical Functional Modeling and Automation Design 4 зан. (2019-02-09 — 2019-05-04)		B16-V74		Yohin M.N.