





D-408. Exam Schedule

Tue, 22 May	16:15 — 17:50	ATT Design of Integrated Circuits and Systems-on-Chip (SOC)  M17-403  Bakerenkov A.S., Shaltaeva Y.R.
Tue, 05 Jun	09:00 — 13:00	EXAM Design of Integrated Circuits and Systems-on-Chip (SOC)  M17-403  Bakerenkov A.S., Shaltaeva Y.R.