







Course Schedule, room D-408

TUESDAY

16:15 — 17:50	<p> <input checked="" type="checkbox"/> LAB Design of Integrated Circuits and Systems-on-Chip (SOC)  M17-403  Shaltaeva Y.R., Bakerenkov A.S. </p> <p> <input checked="" type="checkbox"/> SEM Design of Integrated Circuits and Systems-on-Chip (SOC)  M17-403  Shaltaeva Y.R., Bakerenkov A.S. </p>
17:55 — 18:40	<p> <input checked="" type="checkbox"/> LEC Design of Integrated Circuits and Systems-on-Chip (SOC) (2018-02-06 — 2018-03-27)  M17-403  Bakerenkov A.S., Shaltaeva Y.R. </p>