

V-205. Exam Schedule

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| Fri, 22 Dec | 09:00 – 12:00 | TEST | Electrical Engineering, Electronics, Circuit Design (Electrical Engineering) S16-501 Novozhilov A.E. |
| Fri, 22 Dec | 12:00 – 15:00 | TEST | Electrical Engineering B16-503 Novozhilov A.E. |
| Fri, 22 Dec | 15:00 – 18:00 | TEST | Electrical Engineering B16-513 Novozhilov A.E. |
| Tue, 26 Dec | 09:00 – 12:00 | ATT | Principles of Structure and Design of High-Power Electrophysics Installations S13-001 Lvov E.I. |
| Tue, 26 Dec | 09:00 – 12:00 | ATT | Structure and Design of High-Power Pulsed Electrical Installations S13-001 Lvov E.I. |
| Tue, 26 Dec | 18:00 – 22:00 | ATT | Electrical Engineering B16-V71 Shilov V.K. |
| Wed, 27 Dec | 12:00 – 15:00 | TEST | Computer Simulation and Modeling S14-001 Suhanova L.A. |
| Wed, 27 Dec | 18:00 – 22:00 | ATT | Electrical Engineering B16-V75 Shilov V.K. |
| Thu, 28 Dec | 18:00 – 22:00 | ATT | Electrical Engineering B16-V74 Shilov V.K. |
| Thu, 25 Jan | 09:00 – 13:00 | EXAM | Principles of Structure and Design of High-Power Electrophysics Installations S13-001 Lvov E.I. |
| Thu, 25 Jan | 09:00 – 13:00 | EXAM | Structure and Design of High-Power Pulsed Electrical Installations S13-001 Lvov E.I. |